

Communication Forecasting for Large-Scale Applications



Nikela Papadopoulou, Georgios Goumas, Nectarios Koziris

Computing Systems Laboratory, School of Electrical and Computer Engineering, National Technical University of Athens

Motivation

- The greatest challenge in HPC is to attain petaflop performance on a petaflop machine - and exaflop will be harder!
- Users tend to make overly optimistic assumptions about their applications' scalability - execution results refute their expectations
- A key to attain maximum parallel performance is **predictive modeling** - we need to know what to expect!
- Effective **performance models** for large-scale parallel applications can be a valuable tool for **decision-making** at many levels:
- allocation and utilization of resources by users
- -job scheduling on large-scale systems
- code optimizations (e.g. hybrid MPI/OpenMP, message compression)



for performance tuning and performance portability

- performance auto-tuning at runtime

Challenges

- HPC applications have computation and communication phases.
- Communication on large scale is affected by:
- the data volume
- the communication pattern
- the programming model and communication primitives
- the node architecture
- the network architecture, protocols and topology
- the process mapping on the allocation
- -... who knows what else!
- Computation forecasting seems trivial compared to communication!

Approach

- Topology-agnostic modeling (trade accuracy for generality) • Application-related model variables (easy to extract) • Architecture-related model coefficients (hide complexity)
- Prediction of the per-phase communication time
- Supervised learning (coefficient training with benchmark results)



Methodology

- 1. **Benchmarking** of the system
- 2. Variable selection with statistical analysis
- 3. Model building with forward stepwise regression
- 4. Multiple variable regression to compute model coefficients
- 5. Refinement of the model

Min Error: -60%

Max Error: 48%

Mean Abs Error: 25%

Execution Environment

We experimented on Vilje supercomputer at NTNU (#82 at Top500), an SGI system of 1404 Intel Xeon E5-2670 dual eight-core nodes interconnected with Infiniband FDR on an enhanced hypercube.



Nodes x Procs/Node

communication times against the communication times predicted with our supervised-learning model and with the Hockney model:

 $t_{comm} = Latency + MessageSize/Bandwidth$

Acknowledgements

16x1 16x2 16x4 16x4 16x4 16x4 16x8 12x8 12x8 128x1 128

Nodes x Procs/Node

Min Error: -42%

Max Error: 68%

Mean Abs Error: 24%

This research was partly funded by project I-PARTS (code 2504) of Action ARISTEIA, co-financed by the European Union (European Social Fund) and Hellenic national funds through the Operational Program Education and Lifelong Learning' (NSRF 2007-2013). We would like to thank NTNU, the Norwegian University of Science and Technology at Trondheim, Norway, for granting us access to Vilje Supercomputer.

Future Work

Mean Abs Error: 17%

Min Error: -37%

Max Error: 39%

- Improve model accuracy, by studying and modeling more complex features of communication
- Experiment on different systems and network topologies
- Communication forecasting for applications with more intricate communication patterns
- Extend methodology for collective communication

16X1 16X2 16X2 16X2 16X8 16X4 16X4 16X16 32X1 32X1 32X1 54X1 64X1 64X1 64X1 64X1 64X1 64X1 64X2 64X8 128X1 128X2 128X1 128X2 1

Nodes x Procs/Node

• Automate the performance modeling process and construct a generic, portable tool for performance prediction